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				Application Number	Not Yet Assigned - 10/772,312
				Filing Date	Concurrently Herewith 2.6.04
				First Named Inventor	Greg A. Blodgett
				Art Unit	N/A 2816
				Examiner Name	Not Yet Assigned - ENGLUND
Sheet	1	of	1	Attorney Docket Number	M4065.0495/P495-A

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
**TLE	AA	5,493,249	02/1996	Manning	
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**TLE	AC	6,008,690	12/1999	Takeshima et al.	
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FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
**TLE	AL	"Analog Very Large Scale Integrated Circuits Design of Two-Phase and Multi-phase Voltage Doublers with Frequency Regulation." A Thesis presented to the Faculty of the College of Engineering and Technology, Ohio University, November, 1999	

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*Terry L. Englund 8.5.04*